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JAN 02 2004

APPLICANT:	TSUTOMU ISHIKAWA, ET AL.	) )	OFFICIAL
SERIAL NO.:	09/552,085	Group Art Unit: 2816	OFFICIAL
FILED:	February 24, 2000	Examiner: Nguyen, L.	
IOR:	SEMICONDUCTOR INTEGRATED CIRCUIT HAVING PADS WITH LESS INPUT SIGNAL ATTENUATION	) ) )	

## **AMENDMENT**

Via Facsimile to 703-872-9306 Commissioner for Palents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed September 3, 2003, Applicants request reconsideration in the view of the following amendment and remarks for entry in the above-identified application.

[	hereby certify that this correspondence was facsimile ransmitted to the United States Patent Office (Fax No.
	703-872-9306) on
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١	Signature Date